

### IN THE CLAIMS

Please amend the claims as follows:

1-18. (Canceled)

19. (Previously Presented) An electronic system comprising:

a processor; and

a pre-packaged flip chip coupled to the processor, wherein the flip chip includes:

a first semiconductor device having a first side and a second side, the first side comprising a first array of connection pads, the connection pads electrically coupled to circuits on the first semiconductor device;

an adhesive layer covering the first side of the first semiconductor device with a first surface of the adhesive layer contacting the first side, the adhesive layer having an array of column-shaped openings substantially aligned with one or more connection pads of the first array of connection pads and having a chamfer, opposite the first surface of the adhesive layer, at each of the column-shaped openings; and

a conductive material filling the array of column-shaped openings.

20. (Previously Presented) The electronic system of claim 19, wherein the second side of the first semiconductor device is opposite the first side and includes a protective material substantially covering the second side.

21. (Original) The electronic system of claim 19, wherein the adhesive layer is comprised of one or more film layers.

22. (Original) The electronic system of claim 19, wherein the adhesive layer includes a curable, fluid material.

23. (Original) The electronic system of claim 19, wherein the conductive material is solder.

24. (Original) The electronic system of claim 19, wherein the conductive material is cylindrical in shape.

25-50. (Canceled)

51. (Previously Presented) The electronic system of claim 19, wherein the adhesive layer includes an elastomer.

52. (Previously Presented) The electronic system of claim 19, wherein the adhesive layer includes a thermoplastic material.

53. (Previously Presented) The electronic system of claim 19, wherein the adhesive layer includes a thermoset material.

54. (Previously Presented) The electronic system of claim 19, wherein the adhesive layer includes a pressure-sensitive material.

55. (Previously Presented) The electronic system of claim 20, wherein the protective coating includes an epoxy.

56. (Previously Presented) The electronic system of claim 19, wherein the conductive material includes a conductive paste that hardens upon curing.

57. (Previously Presented) The electronic system of claim 19, wherein the conductive material includes a conductive gel that hardens upon curing.

58. (Previously Presented) The electronic system of claim 19, wherein the conductive material is column-shaped.

59. (Previously Presented) The electronic system of claim 19, wherein the second side of the first semiconductor device includes a bonding layer.

60. (Previously Presented) The electronic system of claim 19, wherein the conductive material is flush with a surface of the adhesive layer opposite the first surface of the adhesive layer.

61. (Previously Presented) The electronic system of claim 19, wherein the conductive material protrudes beyond a surface of the adhesive layer opposite the first surface of the adhesive layer.

62. (Withdrawn) An electronic system comprising:

a processor; and

a pre-packaged flip chip coupled to the processor, wherein the flip chip includes:

a first semiconductor device having a first side and a second side, the first side comprising a first array of connection pads, the connection pads electrically coupled to circuits on the first semiconductor device;

an adhesive layer covering the first side of the first semiconductor device with a first surface of the adhesive layer contacting the first side, the adhesive layer having an array of rectangular openings substantially aligned with one or more connection pads of the first array of connection pads and having a chamfer, opposite the first surface of the adhesive layer, at each of the openings; and

a conductive material filling the array of rectangular openings.

63. (Withdrawn, Currently Amended) The electronic system of claim ~~[[19]]~~ 62, wherein the second side of the first semiconductor device is opposite the first side and includes a protective material substantially covering the second side.

64. (Withdrawn, Currently Amended) The electronic system of claim ~~[[20]]~~ 63, wherein the protective coating includes an epoxy.

65. (Withdrawn, Currently Amended) The electronic system of claim 62, wherein the adhesive layer is comprised of one or more film layers.

66. (Withdrawn, Currently Amended) The electronic system of claim 62, wherein the adhesive layer includes a curable, fluid material.

67. (Withdrawn, Currently Amended) The electronic system of claim 62, wherein the conductive material is solder.

68. (Withdrawn, Currently Amended) The electronic system of claim 62, wherein the adhesive layer includes an elastomer.

69. (Withdrawn, Currently Amended) The electronic system of claim 62, wherein the adhesive layer includes a thermoplastic material.

70. (Withdrawn, Currently Amended) The electronic system of claim 62, wherein the adhesive layer includes a thermoset material.

71. (Withdrawn, Currently Amended) The electronic system of claim 62, wherein the adhesive layer includes a pressure-sensitive material.

72. (Withdrawn, Currently Amended) The electronic system of claim 62, wherein the conductive material includes a conductive paste that hardens upon curing.

73. (Withdrawn, Currently Amended) The electronic system of claim 62, wherein the conductive material includes a conductive gel that hardens upon curing.

74. (Withdrawn, Currently Amended) The electronic system of claim 62, wherein the second side of the first semiconductor device includes a bonding layer.

[[76.]] 75. (Withdrawn, Currently Amended) An electronic system comprising:

a processor; and

a pre-packaged flip chip coupled to the processor, wherein the flip chip includes:

a first semiconductor device having a first side and a second side, the first side comprising a first array of connection pads, the connection pads electrically coupled to circuits on the first semiconductor device;

an adhesive layer covering the first side of the first semiconductor device, the adhesive layer having an array of rectangular openings substantially aligned with one or more connection pads of the first array of connection pads; and

a conductive material filling the array of rectangular openings.

[[77.]] 76. (Withdrawn, Currently Amended) The electronic system of claim [[19]] 75, wherein the second side of the first semiconductor device is opposite the first side and includes a protective material substantially covering the second side.

[[78.]] 77. (Withdrawn, Currently Amended) The electronic system of claim [[77]] 76, wherein the protective coating includes an epoxy.

[[79.]] 78. (Withdrawn, Currently Amended) The electronic system of claim [[19]] 75, wherein the adhesive layer is comprised of one or more film layers.

[[80.]] 79. (Withdrawn, Currently Amended) The electronic system of claim [[19]] 75, wherein the adhesive layer includes a curable, fluid material.

[[81.]] 80. (Withdrawn, Currently Amended) The electronic system of claim [[19]] 75, wherein the conductive material is solder.

[[82.]] 81. (Withdrawn, Currently Amended) The electronic system of claim [[19]] 75, wherein the adhesive layer includes an elastomer.

[[83.]] 82. (Withdrawn, Currently Amended) The electronic system of claim [[19]] 75, wherein the adhesive layer includes a thermoplastic material.

[[84.]] 83. (Withdrawn, Currently Amended) The electronic system of claim [[19]] 75, wherein the adhesive layer includes a thermoset material.

[[85.]] 84. (Withdrawn, Currently Amended) The electronic system of claim [[19]] 75, wherein the adhesive layer includes a pressure-sensitive material.

[[86.]] 85. (Withdrawn, Currently Amended) The electronic system of claim [[19]] 75, wherein the conductive material includes a conductive paste that hardens upon curing.

[[87.]] 86. (Withdrawn, Currently Amended) The electronic system of claim [[19]] 75, wherein the conductive material includes a conductive gel that hardens upon curing.

[[88.]] 87. (Withdrawn, Currently Amended) The electronic system of claim [[19]] 75, wherein the second side of the first semiconductor device includes a bonding layer.

88. (New) The electronic system of claim 19, wherein the conductive material and the adhesive layer are free from an underfill.